



S&H Form: (2/01)  
Attorney Docket No. 1076.1094

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Takashi KURIHARA, et al.

Application No.: 10/823,649

Group Art Unit: 2825

Confirmation No.: 4917

Filed: April 14, 2004

Examiner: DOAN, NGHIA M.

For: METHOD AND APPARATUS FOR DESIGNING SEMICONDUCTOR INTEGRATED  
CIRCUIT

**RESPONSE TO RESTRICTION REQUIREMENT**

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed January 29, 2007, applicants elect Group I,  
claims 1-11, 21 and 24, without traverse.

Respectfully submitted,

STAAS & HALSEY LLP

Date:

February 23, 2007

By:

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